

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/541,494	07/07/2005	Masanori Hongo	050333	1130
23850 7590 01/03/2007 ARMSTRONG, KRATZ, QUINTOS, HANSON & BROOKS, LLP			EXAMINER	
ARMSTRONG, KRATZ, QUINTOS, HANSON & BROOKS, LLP 1725 K STREET, NW SUITE 1000 ART UNIT PAPER NUMBER	FONG FONG			
SUITE 1000 WASHINGTON, DC 20006			ART UNIT	PAPER NUMBER
			1775	
SHORTENED STATUTOR	RY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE	
3 MC	NTHS	01/03/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary Examiner						
Cathy Lam 1775 The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If INO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication to reply within the set or extended period for-reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). Status 1) Responsive to communication(s) filed on 27 November 2006. 2a) This action is FINAL. 2b) This action is non-final. 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213. Disposition of Claims 4) Claim(s) 1-20 is/are pending in the application. 4a) Of the above claim(s) 4-8 and 10-20 is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1-3 and 9 is/are rejected. 7) Claim(s) is/are objected to.						
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Application Papers						
 9) The specification is objected to by the Examiner. 10) The drawing(s) filed on <u>07 July 2005</u> is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152. 						
Priority under 35 U.S.C. § 119	•					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
Attachment(s) Notice of References Cited (PTO-892)						

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Election/Restrictions

Applicant's election without traverse of group I in the reply filed on November 27,
 acknowledged.

2. This application contains claims 4-8 & 10-20 are drawn to an invention nonelected without traverse. A complete reply to the final rejection must include cancellation of nonelected claims or other appropriate action (37 CFR 1.144) See MPEP § 821.01.

Claim Rejections - 35 USC § 112

3. Claims 1-3 and 9 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 1, the phrases "the side edge electrode layer comprising a parallel wall unexposed and approximately parallel to a side surface of" And "a perpendicular wall approximately perpendicular to the side surface of the laminated ceramic substrate" are structurally unclear. Applicant is required to clarify the structure.

Claim 2 is vague and indefinite, as to what is "an R-shape" and "R is greater than 0.02 mm". Clarification is required.

Clams 3 and 9 are vague and indefinite, as it is unclear what "a sum of depths of opposite....partially differs with respect to a laminated direction" is referring to?

Clarification is required.

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Claim Rejections - 35 USC § 102/103

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 6. Claims 1-3 and 9 are rejected under 35 U.S.C. 102(b) as anticipated by or, in the alternative, under 35 U.S.C. 103(a) as obvious over Yokotani et al (US 5512253).

Due to the invention is unclear, with respect to the prior art rejections below, the claims will be given the broadest possible interpretation in view of the specification.

Yokotani teaches a ceramic substrate for an electronic circuit. The ceramic substrate is comprised of a plurality of ceramic sheets (5) and conductive layers (1,2,3,4) (col 6 L 59-60). The ceramic sheets (5) and the conductive layers are alternately laminated together (Fig. 1A).

Terminal electrodes (8), which are made from the same material as the conductive layers, are laminated at two ends of the laminated body and electrically connected with the conductive layers (col 7 L 13-15).

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Although Yokotani is silent about the dimensions of the terminal electrode on the outermost surface of the laminated body. From Figure 1B, it is clearly shown that the length is greater than the depth.

Regarding to the R value, applicant is urged to clarify what "R shape" is referring to. The examiner is taking the position that such R value is an arbitrary value that one can figure it out by routine experimentations.

7. Claims 1-3 & 9 are rejected under 35 U.S.C. 102(b) as anticipated by or, in the alternative, under 35 U.S.C. 103(a) as obvious over Piper (US 3612963).

Piper teaches a multilayer ceramic capacitor which is comprised of a plurality of ceramic sheets (12) and a plurality of conductive layers (or internal electrodes) (13&15).

The ceramic sheets (12) and conductive layers (13&15) are alternately laminated to form a ceramic stack (11) (Figs. 1 & 2, col 3 L 45-50). Metallization coatings (17&18) are formed onto the end faces of the ceramic stack (11), to form a side (or external) electrode (col 4 L 38-42 & Fig. 3).

Although Piper does not specifically teach the dimensions of the side electrode, but it is clearly shown in Figs. 3-5 that the length on the outer surface is greater than the depth on the outer surface of the side electrode.

The examiner is taking the position that both Yokotani and Piper teach the concept of the present invention.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Cathy Lam whose telephone number is (571) 272-1538. The examiner can normally be reached on 9am-6pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jennifer McNeil can be reached on (571) 272-1540. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Cathy Lam

Primary Examiner
Art Unit 1775

cfl

December 22, 2006